

METHOD AND CIRCUIT FOR OPTIMIZING POWER CONSUMPTION AND PERFORMANCE OF DRIVER CIRCUITS

Abstract of Disclosure

A method and circuit for optimizing power consumption and performance of driver circuits are described. More particularly, embodiments of the present invention provide an enhanced driver circuit. The enhanced driver circuit provides a first voltage, and a detector coupled to the enhanced driver that monitors the first voltage. If the first voltage falls below a predetermined value, the enhanced driver increases the first voltage to at least an optimal voltage.